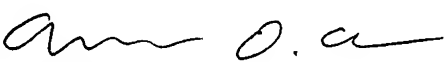


# ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	METHOD AND STRUCTURE FOR PROVIDING TUNED LEAKAGE CURRENT IN CMOS INTEGRATED CIRCUIT						
<p>Application Number : 10/710,006 A.O.A.</p> <p>Confirmation Number:</p> <p>First Named Applicant: Brent Anderson</p> <p>Attorney Docket Number: BUR920040019US1</p> <p>Art Unit:</p> <p>Examiner:</p> <p>Search string: ( 4827324 or 5264380 or 5321283 or 6509233 or 20020123196 ).pn</p>							
<b>US Patent Documents</b>							
<p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p>							
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
A.O.A.	1	4827324	1989-05-02	Blanchard			
A.O.A.	2	5264380	1993-11-23	Pfiester			
A.O.A.	3	5321283	1994-06-14	Cogan et al			
A.O.A.	4	6509233	2003-01-21	Chang et al			
<b>US Published Applications</b>							
<p>Note: Applicant is not required to submit a paper copy of cited US Published Applications</p>							
init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
A.O.A.	1	20020123196	2002-09-05	Chang et al			
<b>Signature</b> 							
Examiner Name				Date			
Andrew O. Arena				24 June 2005			